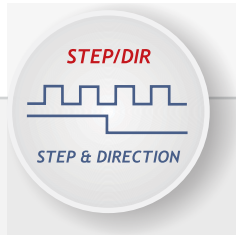


# HGD Series Drives



## INTRODUCTION

- Series of ministepp bipolar chopper drives, suitable for driving medium power two-phase stepping motors, with four, six or eight terminals.
- Highly compact (70x70x25 mm), easy to use and cost effective solution. This system is designed to be soldered to a PCB.
- Target: medium and medium-low power applications requiring increase in performance compared to self-built or integrated circuits combined with an improvement of reliability and durability.

## HIGHLIGHTS

- Microstepping function up to 3.200 step/rev.
- Separated solder type connectors for logic signals and power connections.
- Electronic damping facility for further acoustic noise and mechanic vibrations reduction at low and medium speed.
- Standard input and output signals ease interfacing with the most commonly used control systems and ensure high noise immunity.

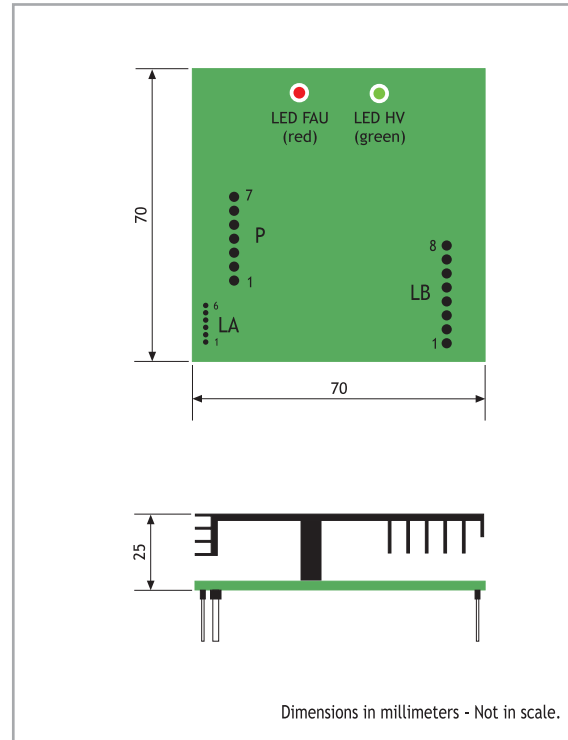
Series	Model	V <sub>DC</sub> range (Volt)	I <sub>NP</sub> min. (Peak value) (Amp)	I <sub>NP</sub> max. (Peak value) (Amp)	Dimensions (mm)
HGD	02	24 to 75	0.75	2.0	70x70x25
HGD	05	24 to 75	2.25	6.0	70x70x25

## TECHNICAL FEATURES

- Range of operating voltages: 24-75 V<sub>DC</sub>.  
Operation with a single external supply voltage.
- Range of current: 0.75-6.0 Amp. Setting up to six possible values by means of hardware connections.
- Microstepping: 400, 800, 1.600 and 3.200 steps /revolution. Setting by means of hardware connections.
- Automatic current reduction at motor standstill.
- Protections:
  - Protection against under-voltage and over-voltage.
  - Protection against a short-circuit at motor outputs.
  - Overtemperature protection.
- Possibility to reduce motor current with an external logic signal.
- High efficiency CHOPPER with MOSFET final stage output.
- Electronic damping facility for further acoustic noise and mechanic vibrations reduction at low and medium speed.
- Warranty: 24 months.



## MECHANICAL DIMENSIONS



INTRODUCTION

STEP & DIRECTION

ADVANCED

ANALOG INPUT

PROGRAMMABLE

EtherCAT

CANopen

## POWER AND LOGIC CONNECTIONS

